July 2007



# MPSA13 NPN Darlington Transistor

- This device is designed for applications requiring extremely high Current gain at collector Currents to 1.0A.
- Sourced from process 05.



1. Emitter 2. Base 3. Collector

# Absolute Maximum Ratings $T_a = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Value	Units
V <sub>CES</sub>	Collector-Emitter Voltage	30	V
V <sub>CBO</sub>	Collector-Base Voltage	30	V
V <sub>EBO</sub>	Emitter-Base Voltage	10	V
I <sub>C</sub> Collector Current - Continuous		1.2	A
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range	-55 to +150	°C

# **Electrical Characteristics** $T_a=25^{\circ}C$ unless otherwise noted

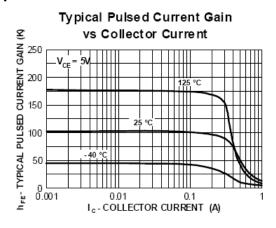
Symbol	Parameter	Test Condition	Min.	Max.	Units
Off Charac	teristics				
V <sub>(BR)CES</sub>	Collector-Emitter Breakdown Voltage	$I_{\rm C} = 100 \mu {\rm A}, I_{\rm B} = 0$	30		V
I <sub>CBO</sub>	Collector-Cutoff Current	$V_{CB} = 30V, I_E = 0$		100	nA
I <sub>EBO</sub>	Emitter-Cutoff Current	$V_{EB} = 10V, I_{C} = 0$		100	nA
On Charact	teristics *				
h <sub>FE</sub>	DC Current Gain	$V_{CE} = 5.0V, I_C = 10mA$ $V_{CE} = 5.0, I_C = 100mA$	5,000 10,000		
V <sub>CE (sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 100mA, I <sub>B</sub> = 0.1mA		1.5	V
V <sub>BE (on)</sub>	Base-Emitter On Voltage	I <sub>C</sub> = 100mA,V <sub>CE</sub> = 5.0V		2.0	V
Small Sign	al Characteristics		•		•
f <sub>T</sub>	Current Gain Bandwidth Product	I <sub>C</sub> = 10mA, V <sub>CE</sub> = 10V, f = 100MHz	125		pF

\* Pulse Test: Pulse Width $\leq$ 300 $\mu$ s, Duty Cycle $\leq$ 2%

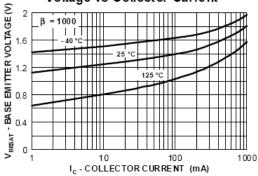
Symbol	Parameter	Max.	Units
P <sub>D</sub>	Total Device Dissipation Derate above 25°C	625 5.0	mW mW/°C
$R_{ ext{ heta}JC}$	Thermal Resistance, Junction to Case	83.3	°C/W
$R_{ ext{ heta}JA}$	Thermal Resistance, Junction to Ambient	200	°C/W

\* Device mounted on FR-4PCB 1.6"  $\times$  1.6"  $\times$  0.06".

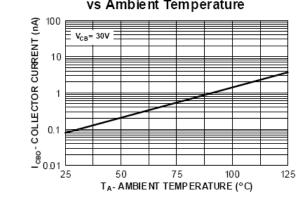
### **Typical Characteristics**

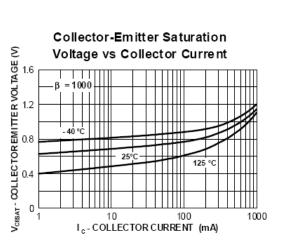


Base-Emitter Saturation Voltage vs Collector Current

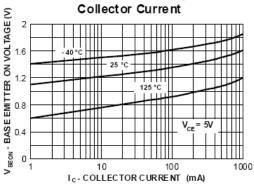


Collector-Cutoff Current vs Ambient Temperature

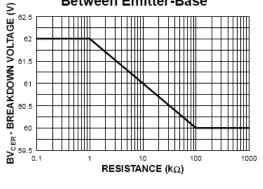


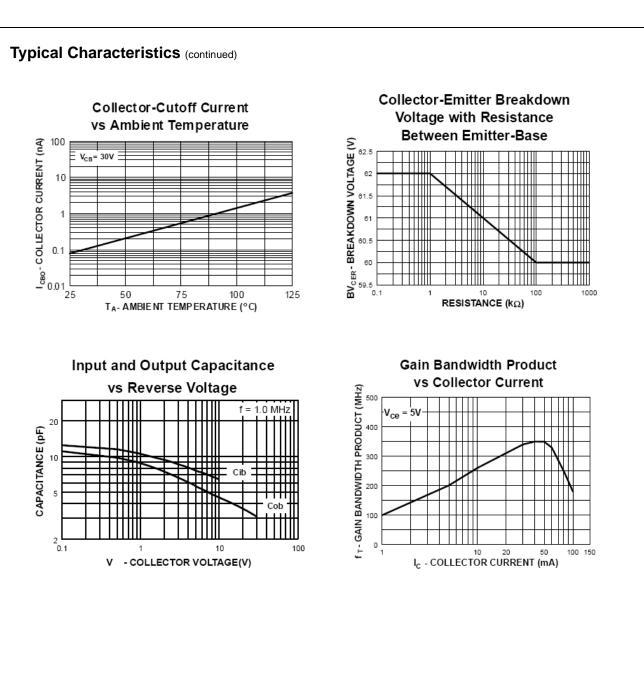


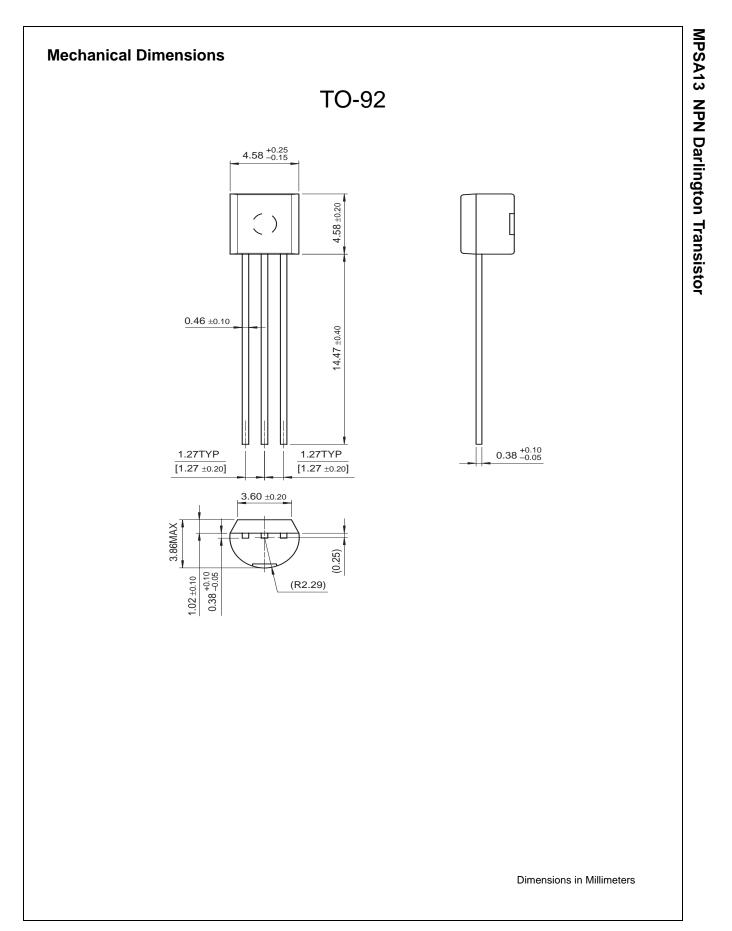
Base Emitter ON Voltage vs



Collector-Emitter Breakdown Voltage with Resistance Between Emitter-Base







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